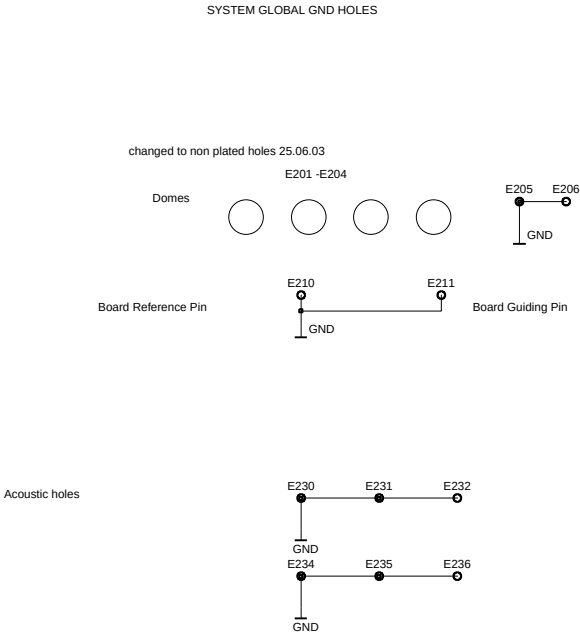
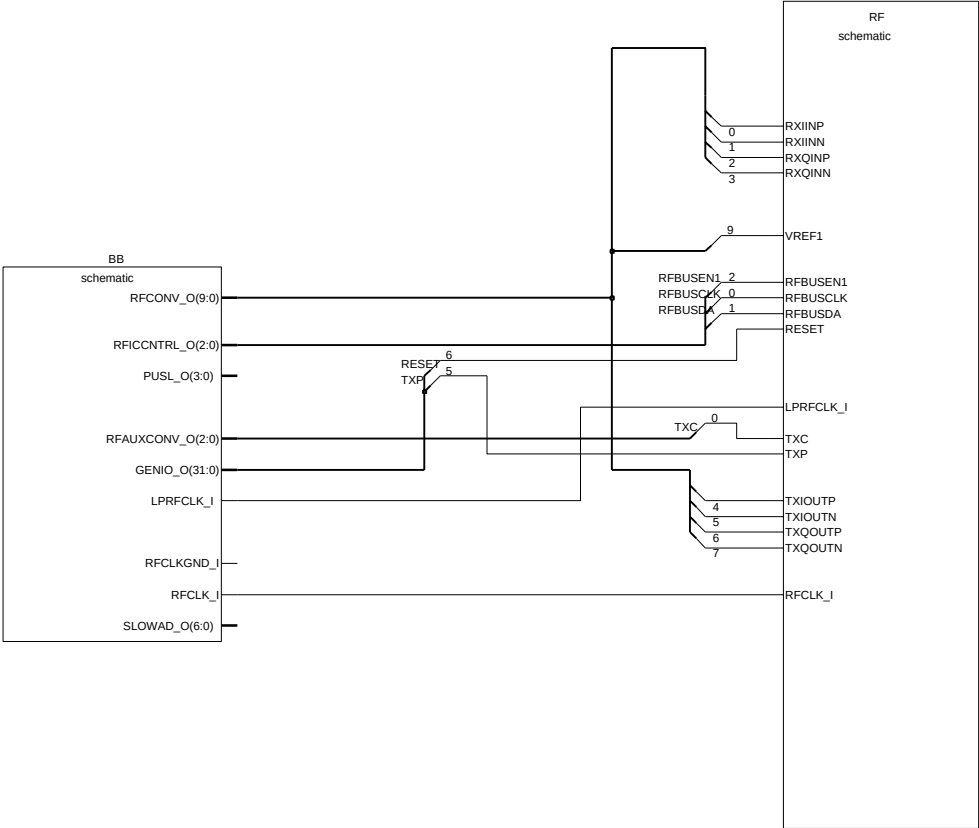
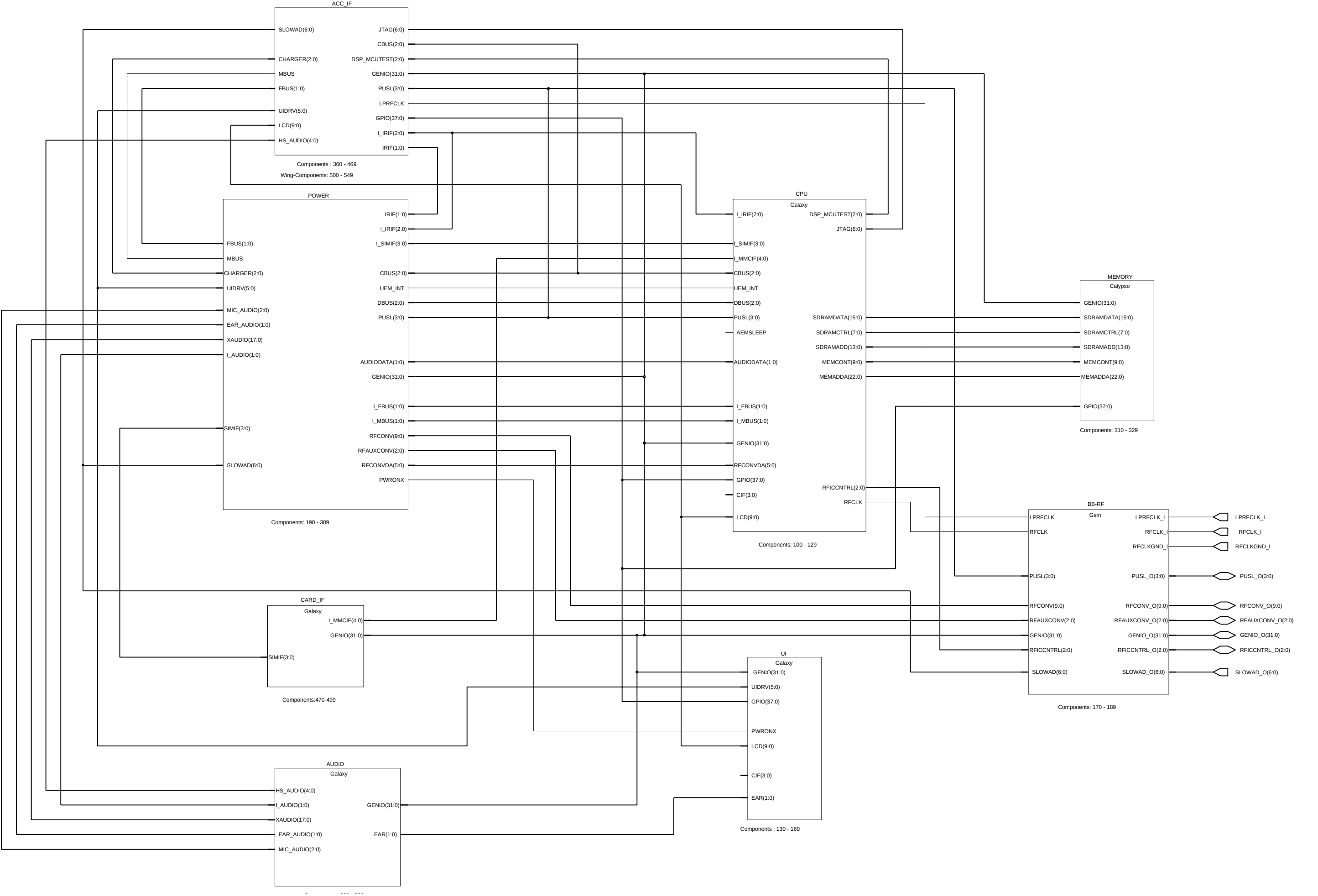
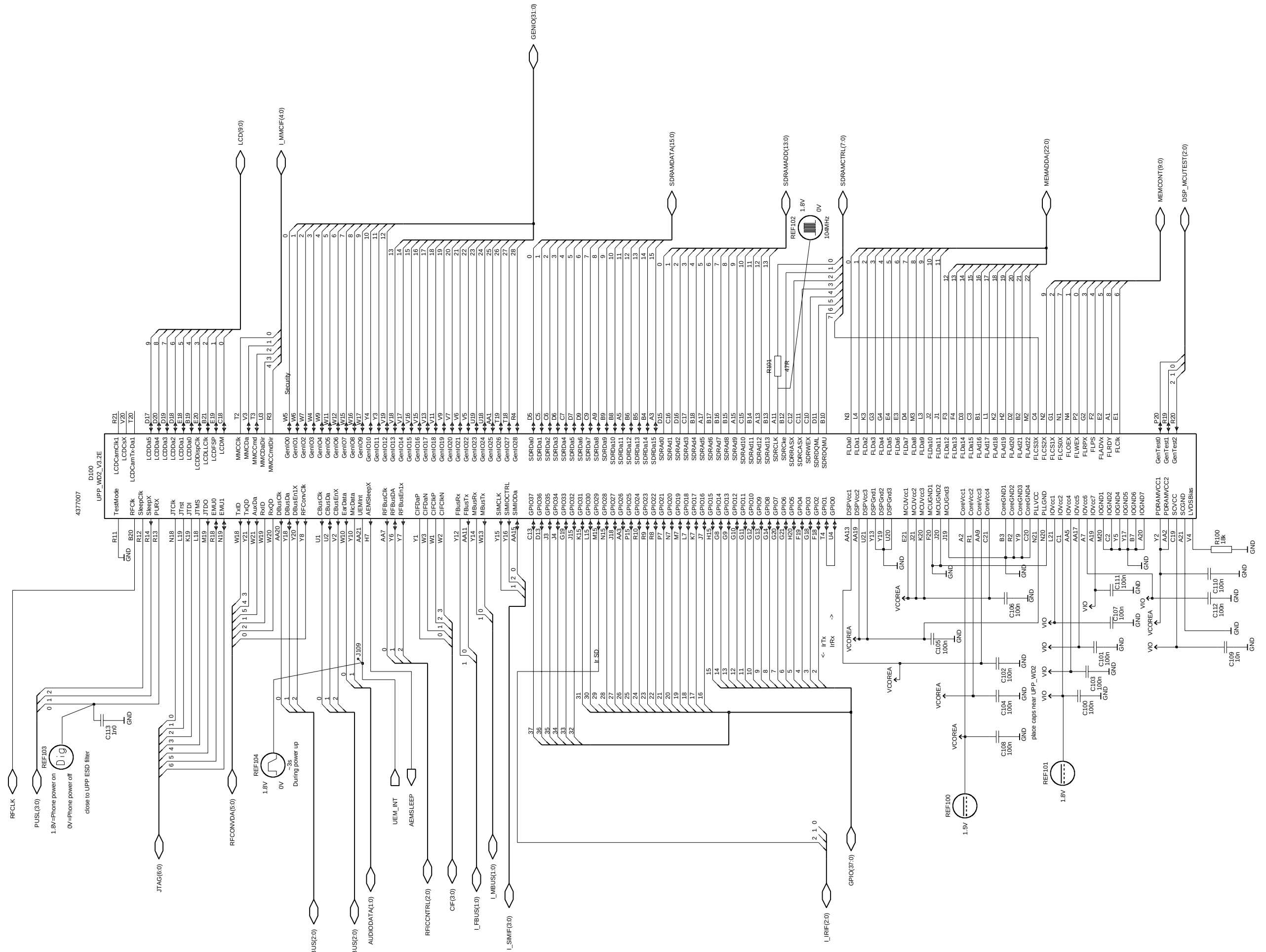
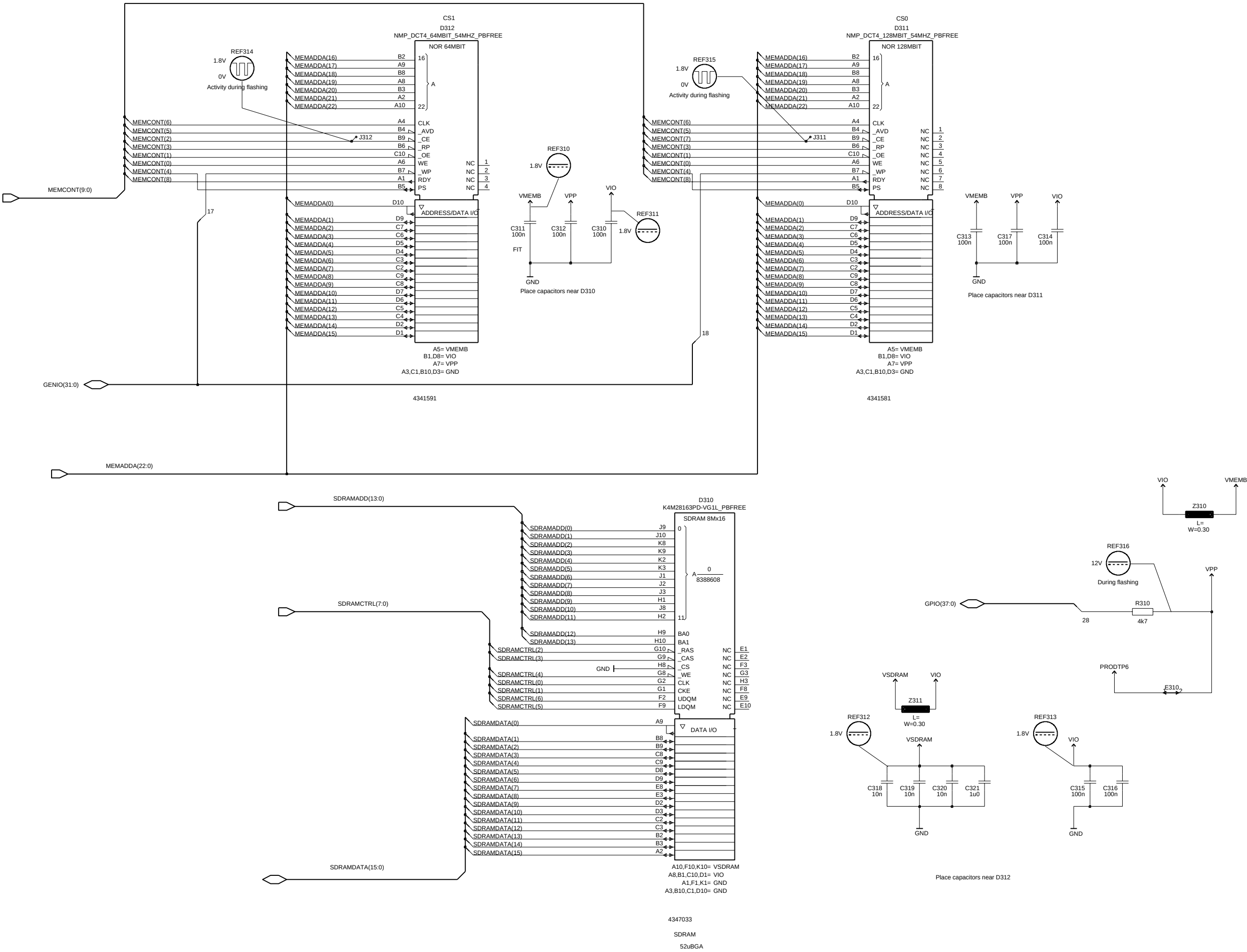


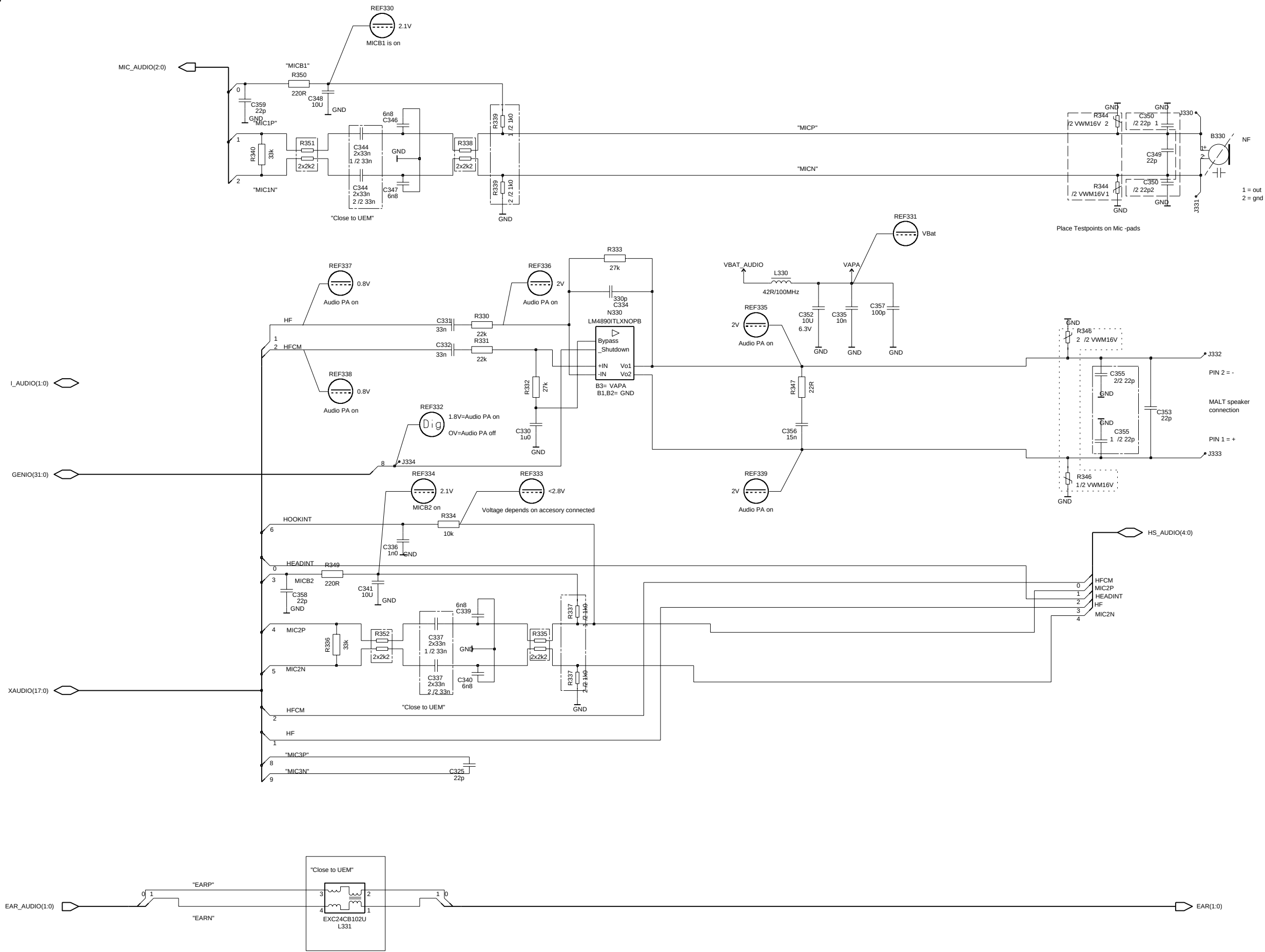
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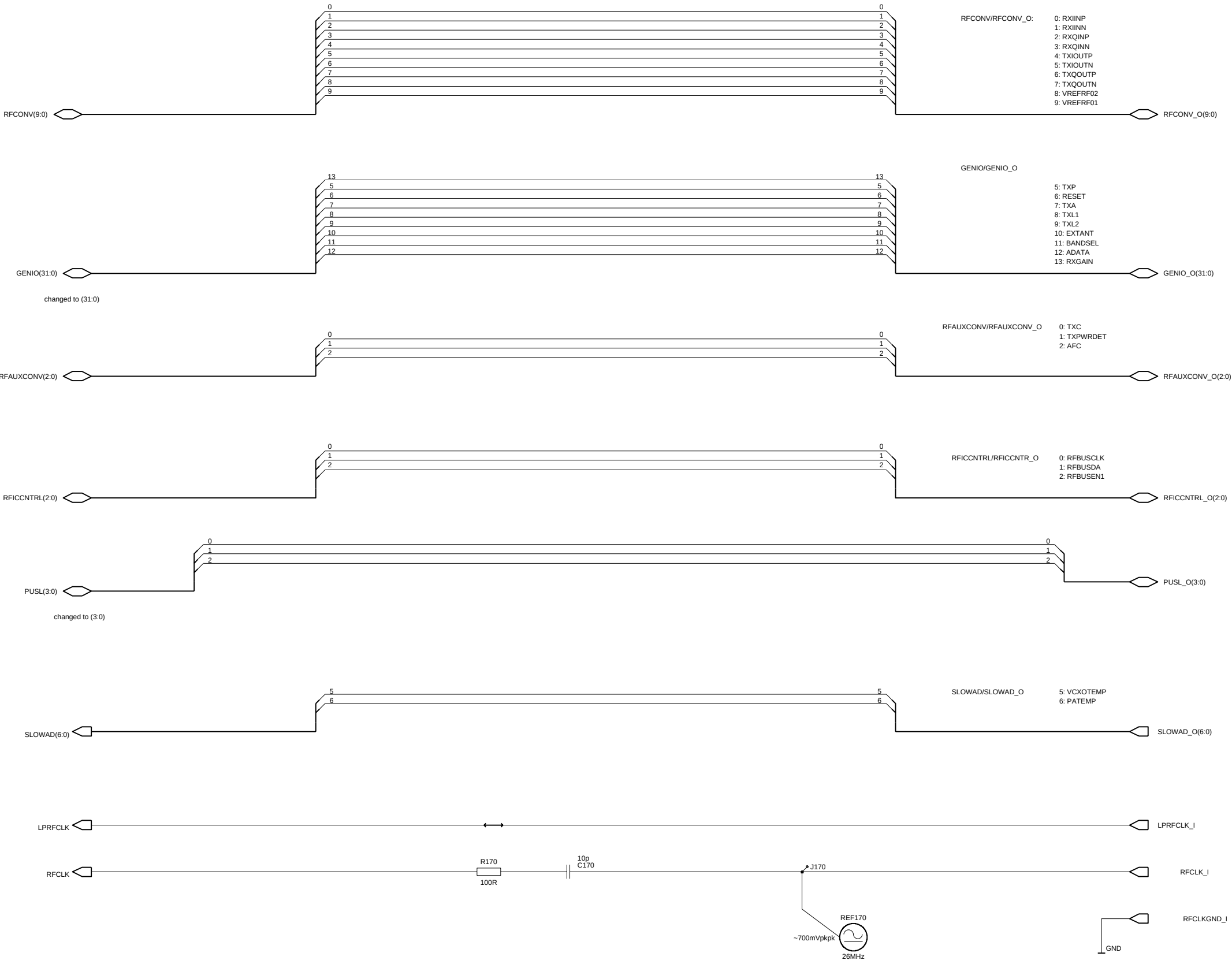


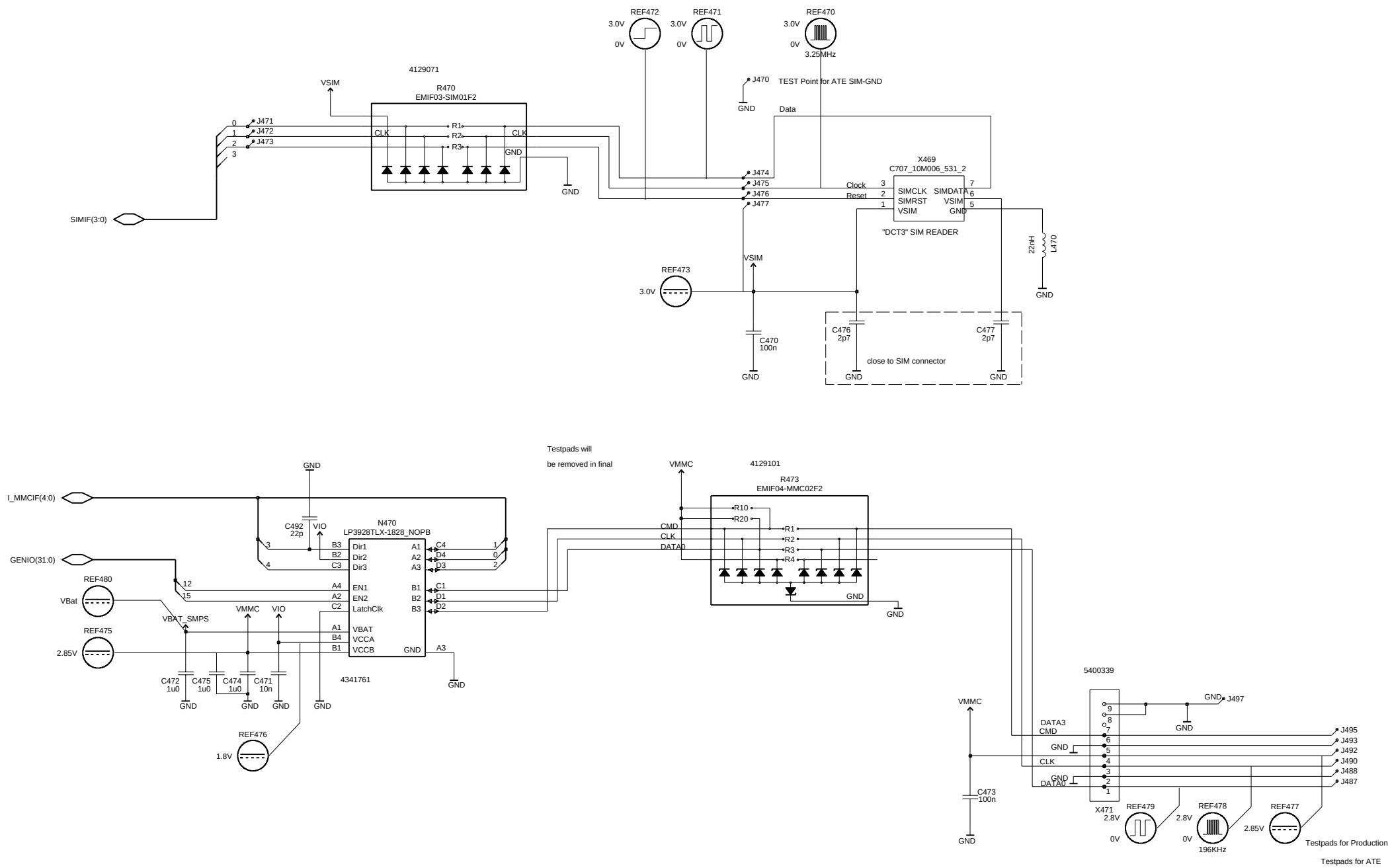


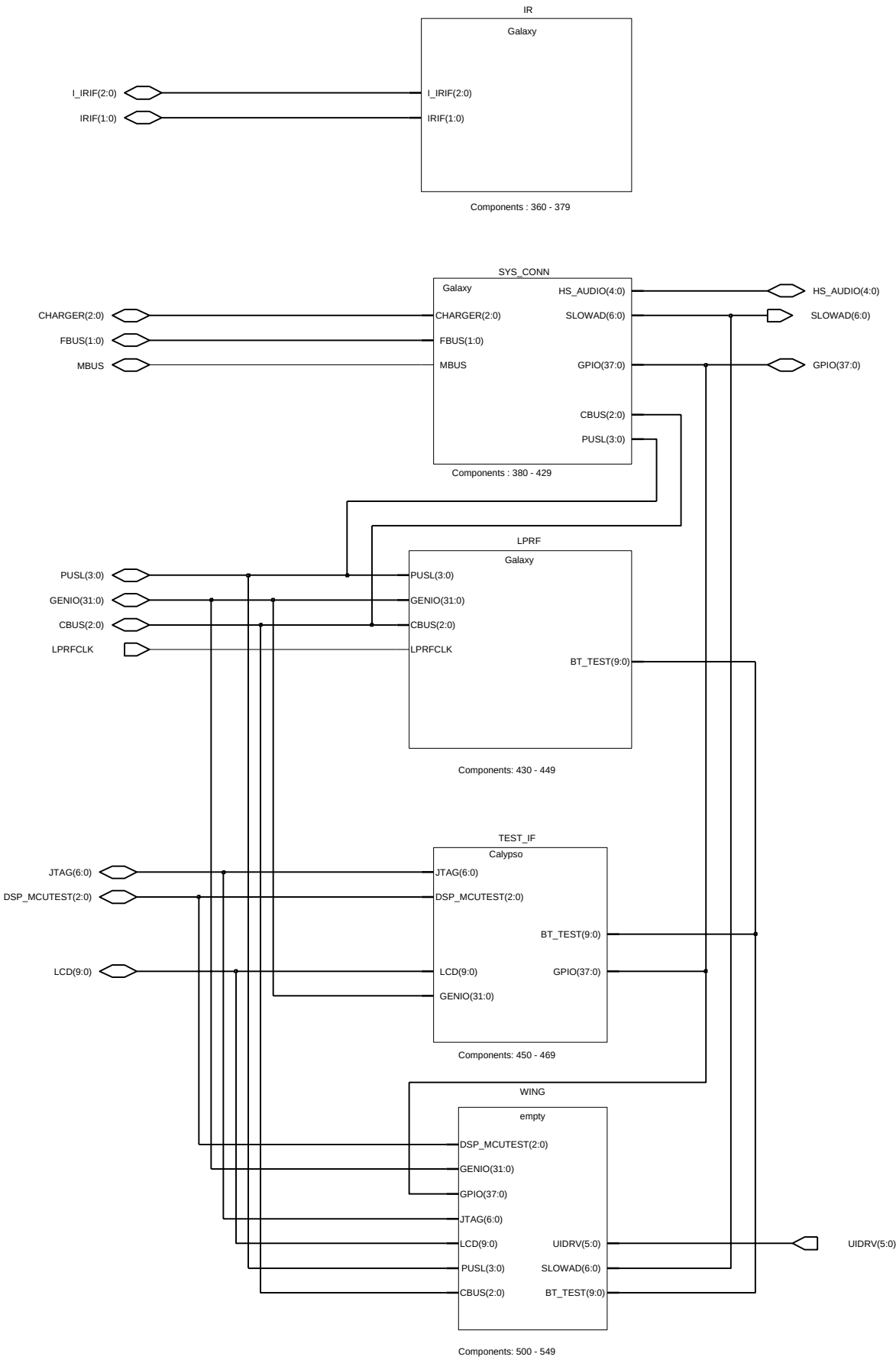


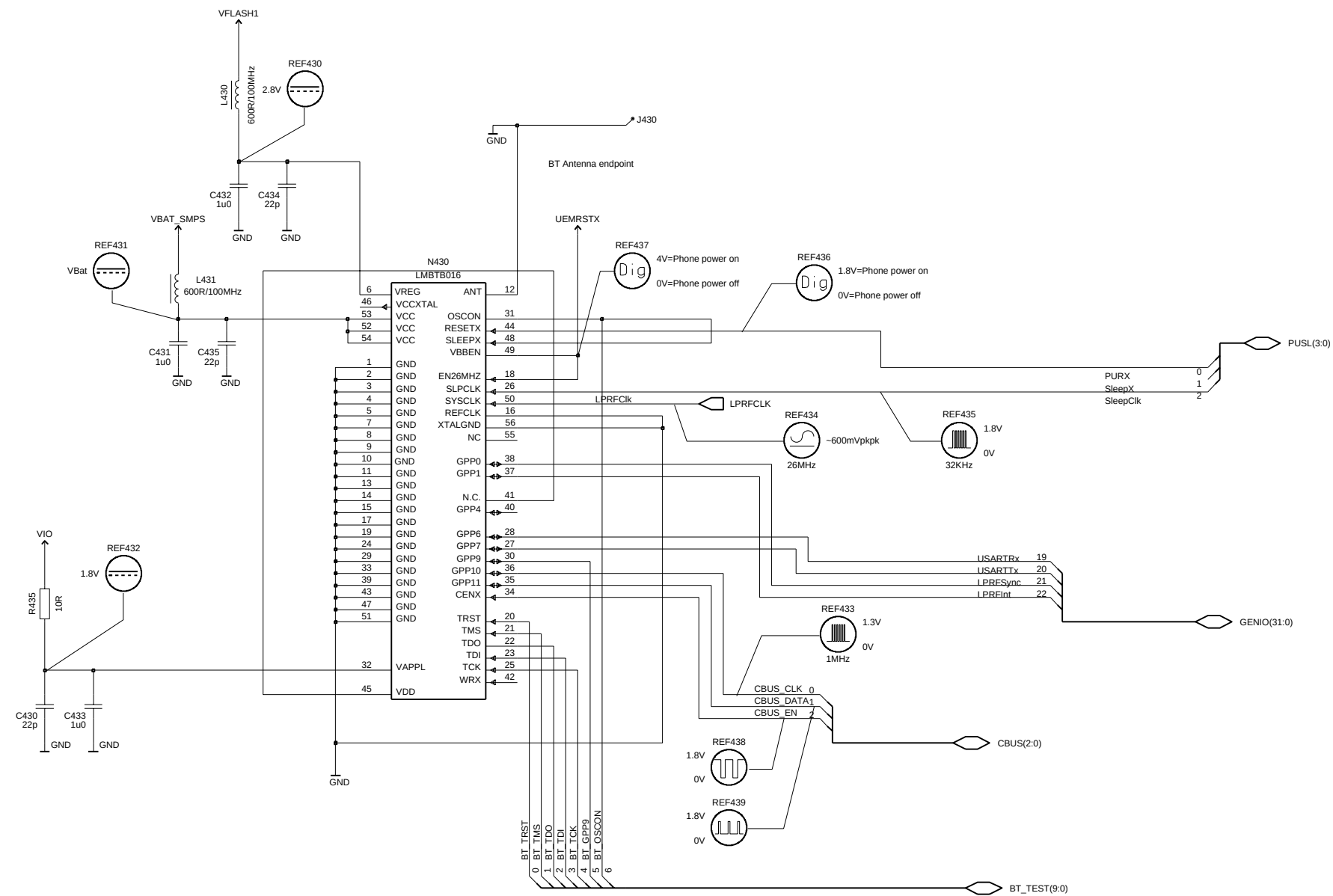


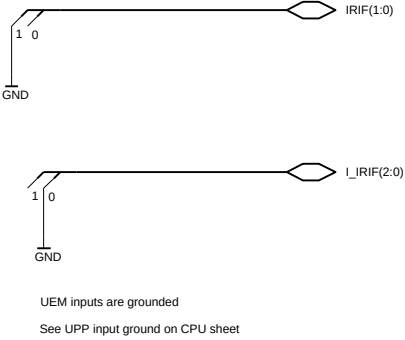


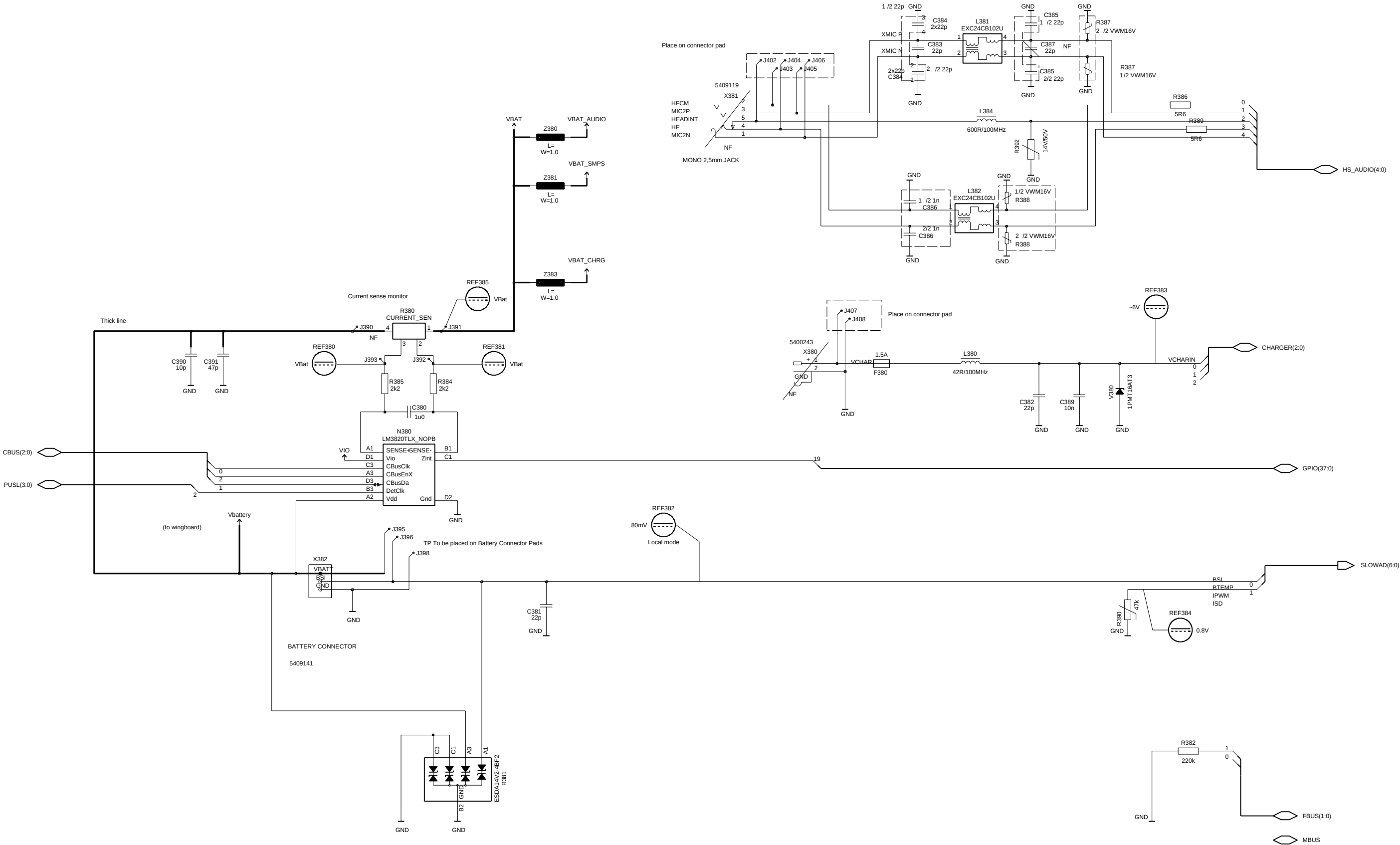




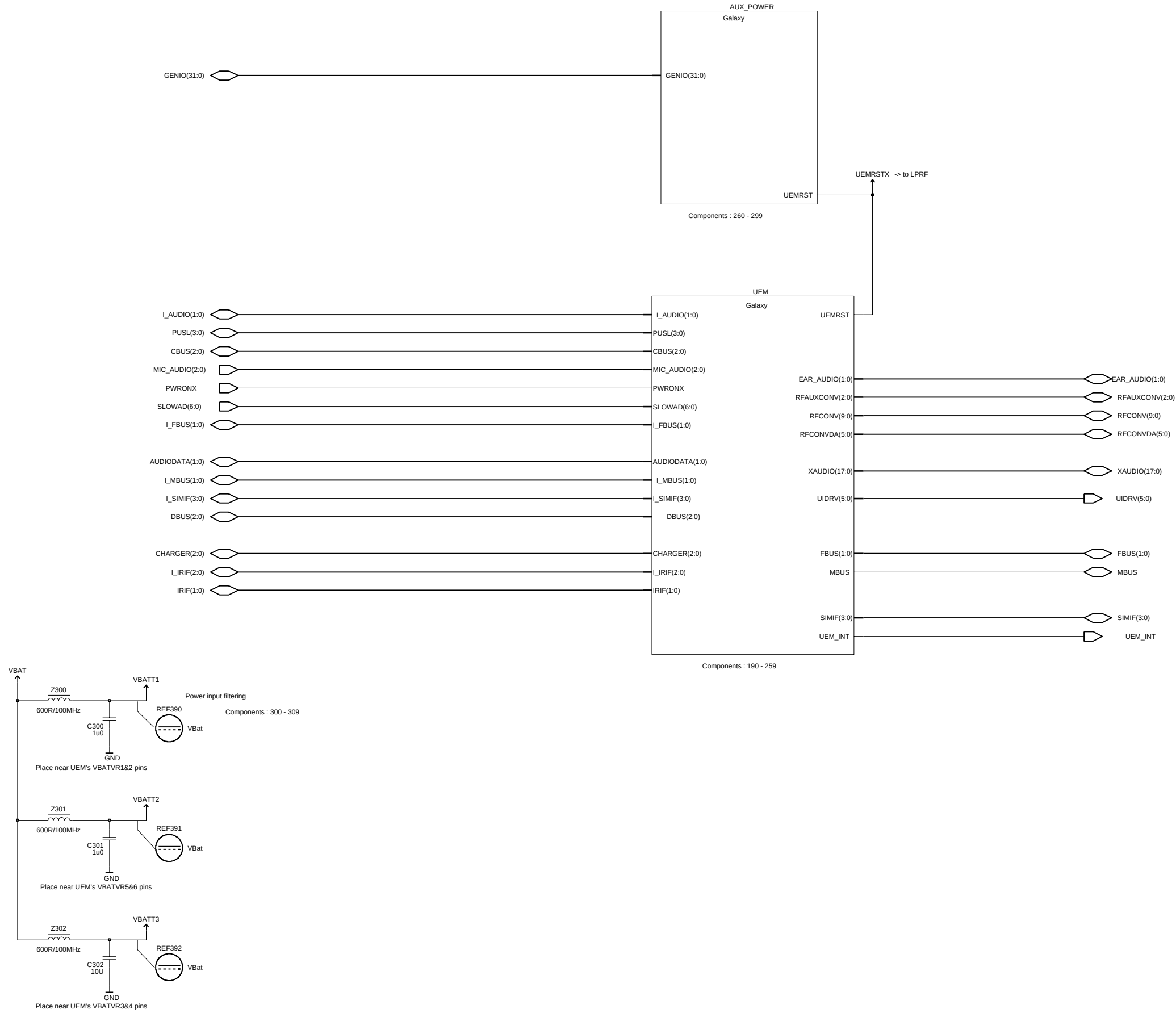




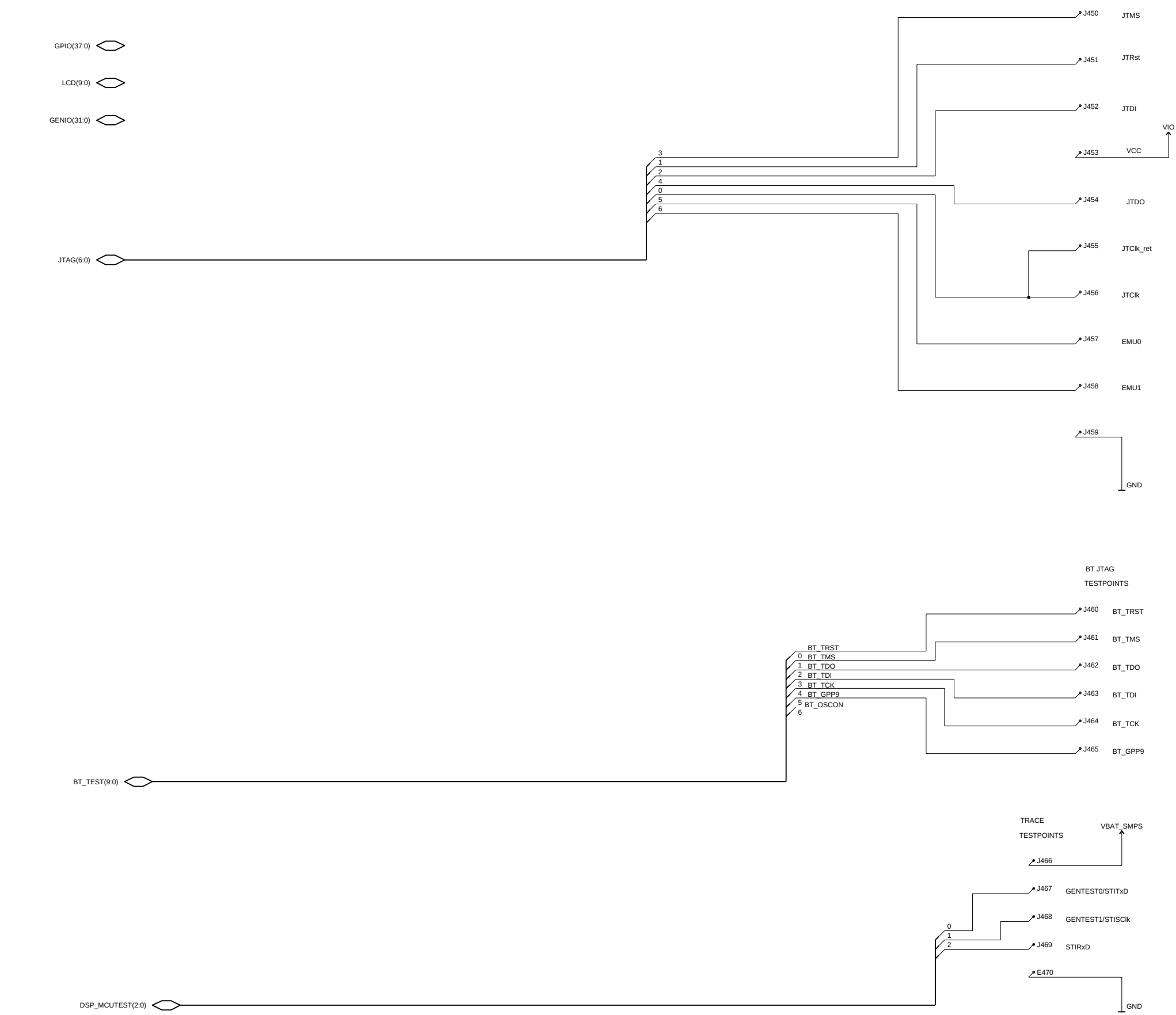




Power diagram, v. 1.0 ed.12

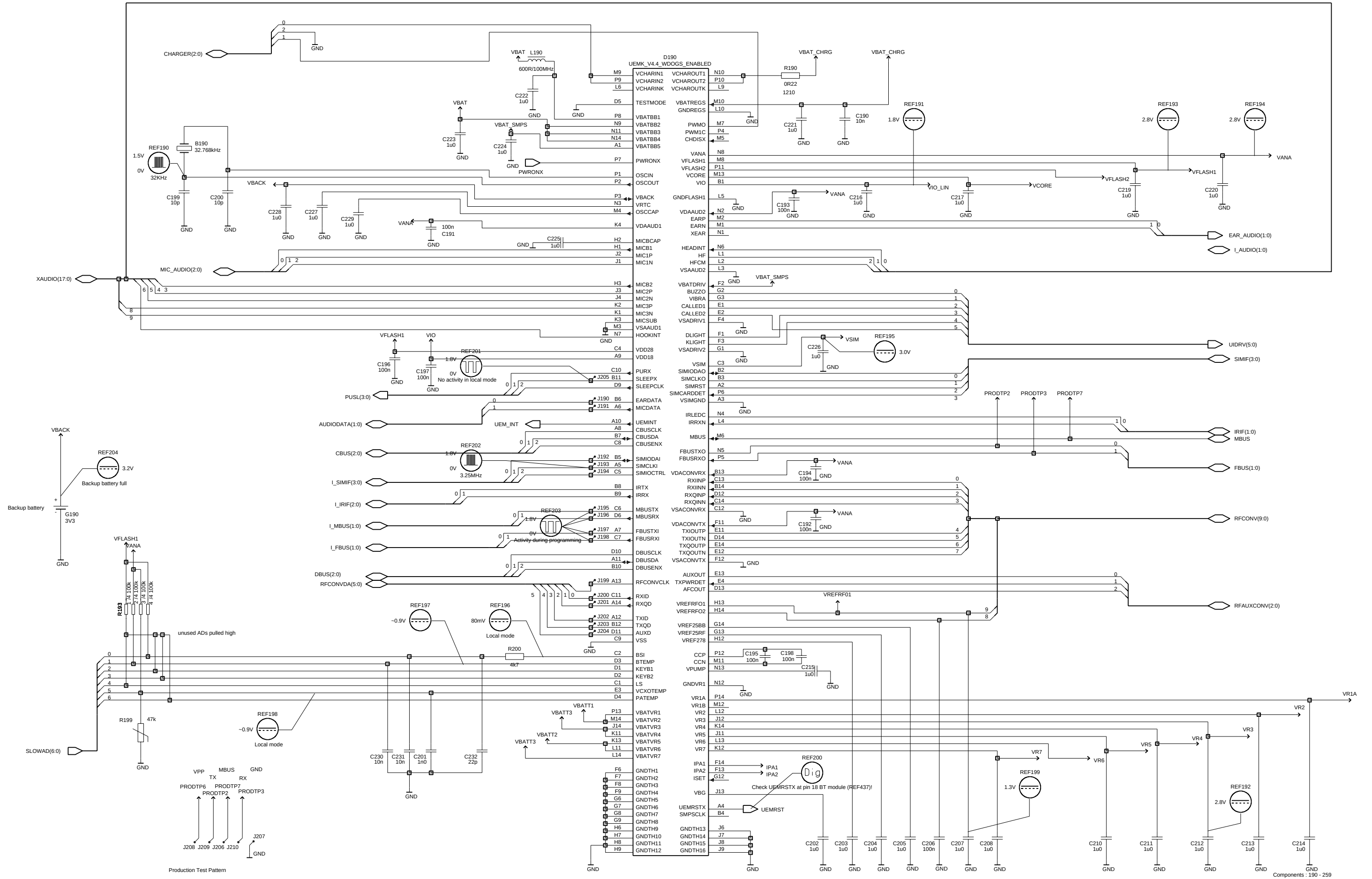


Test interface diagram v. 0.1 ed.7



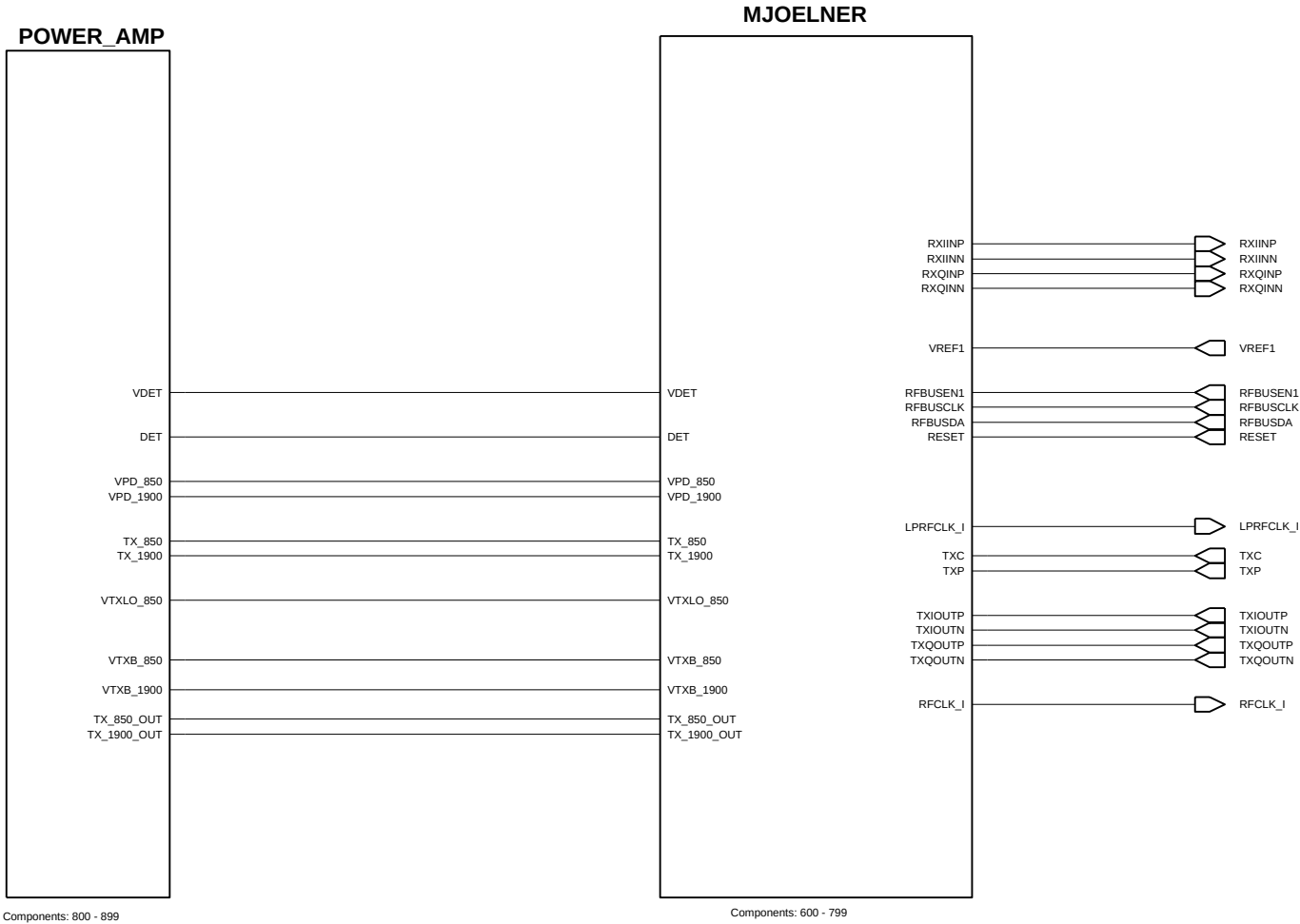
Components: 450 - 469

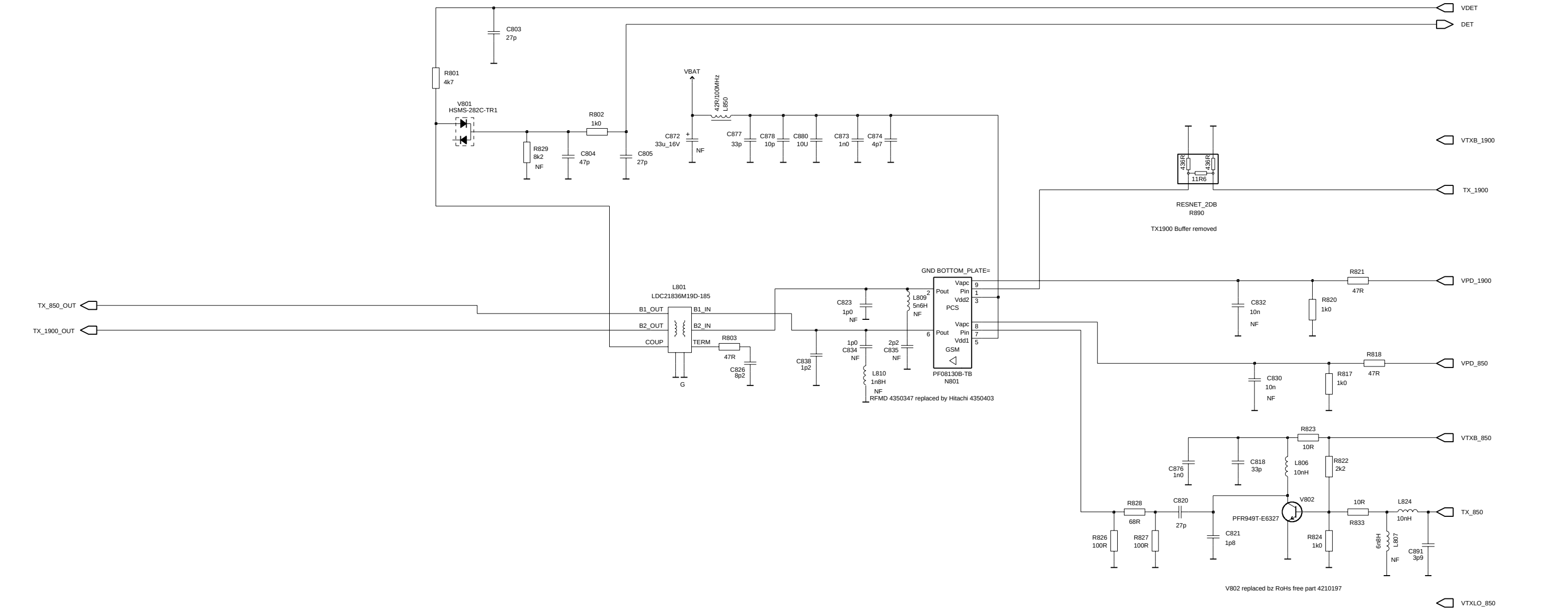
UEM diagram, v.1.0 ed.16



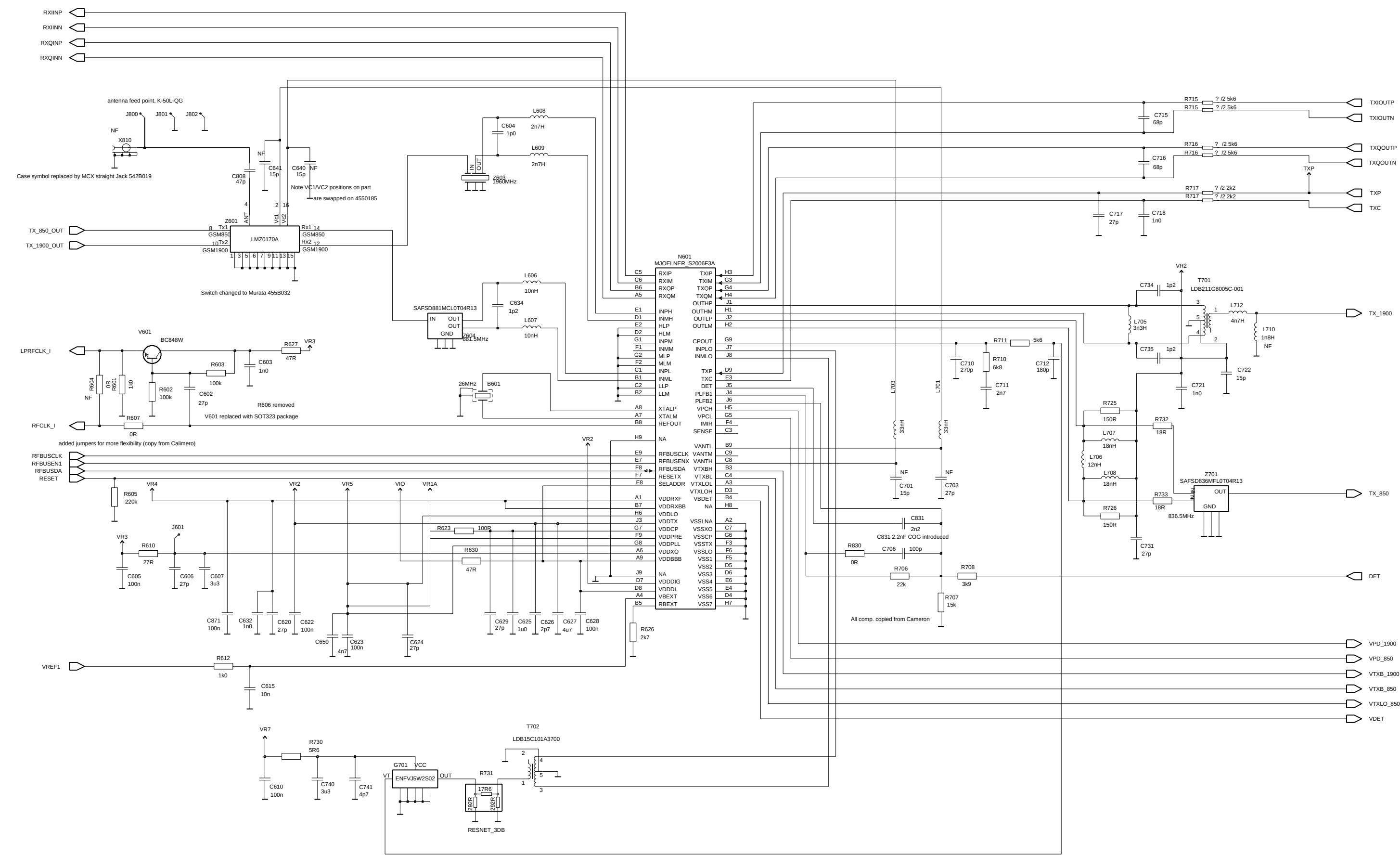
Aux_Power diagram, v.1.0 ed.11

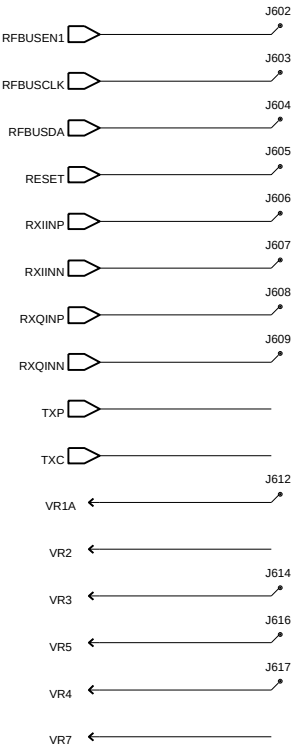




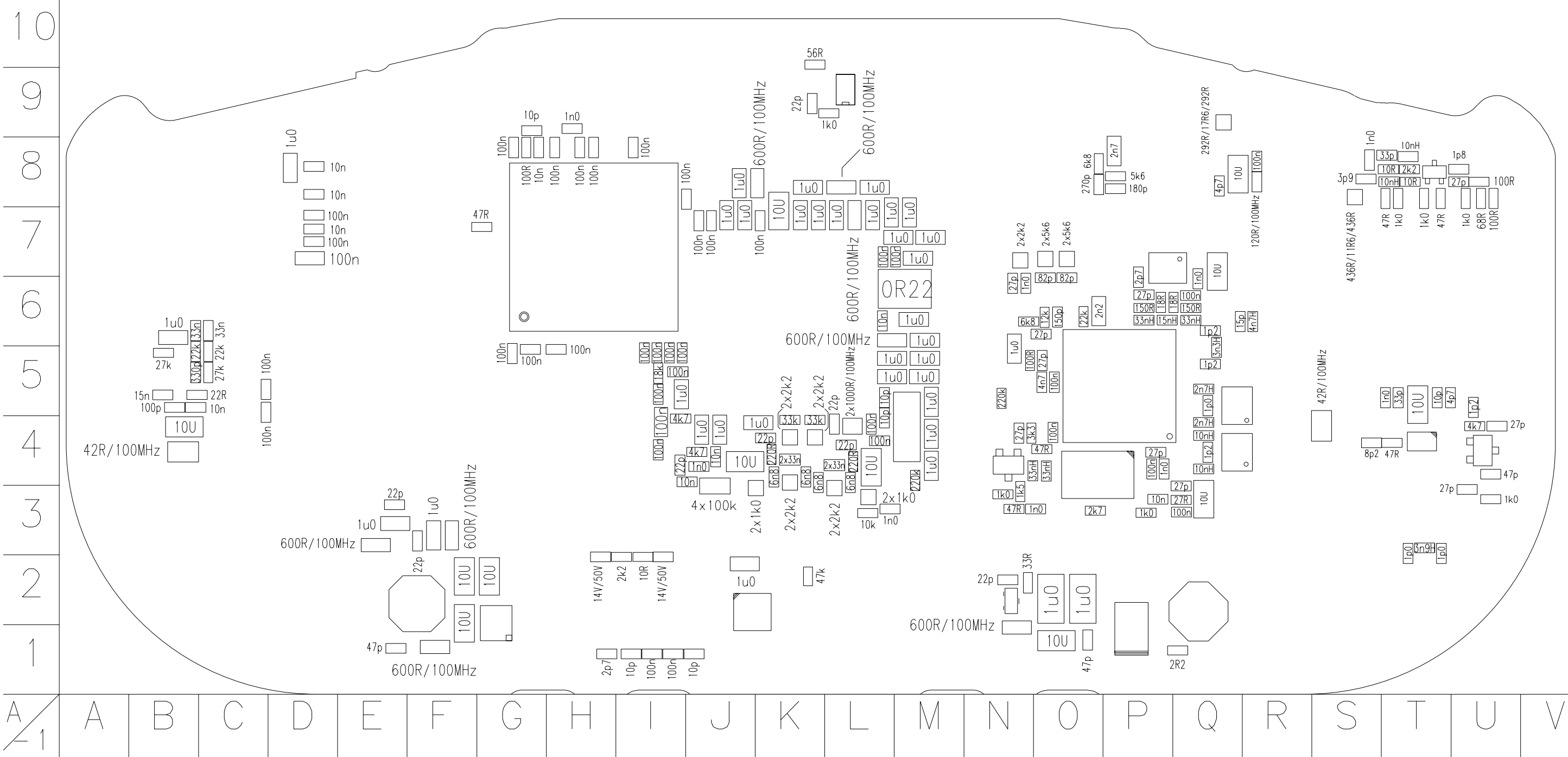


Components: 800 - 899

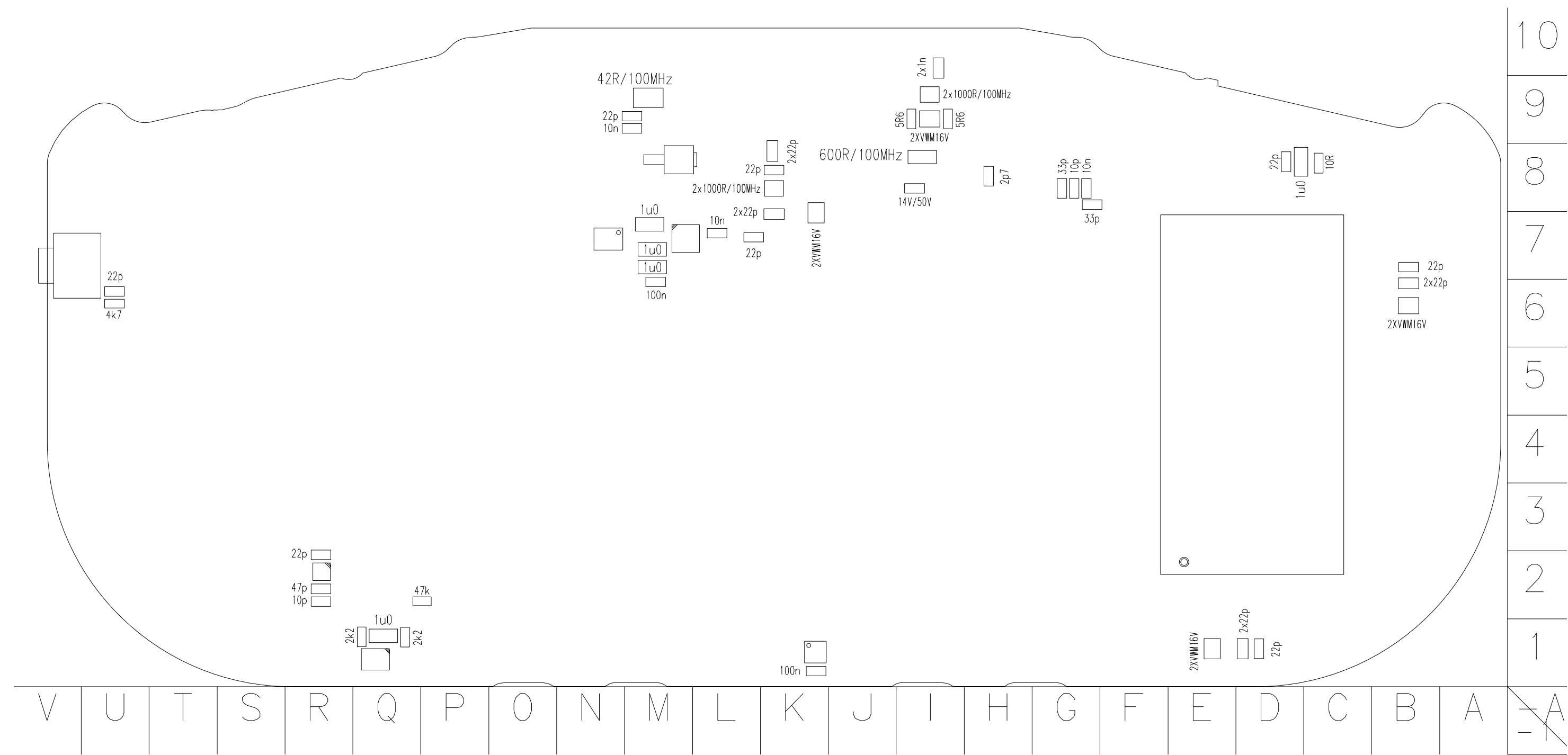




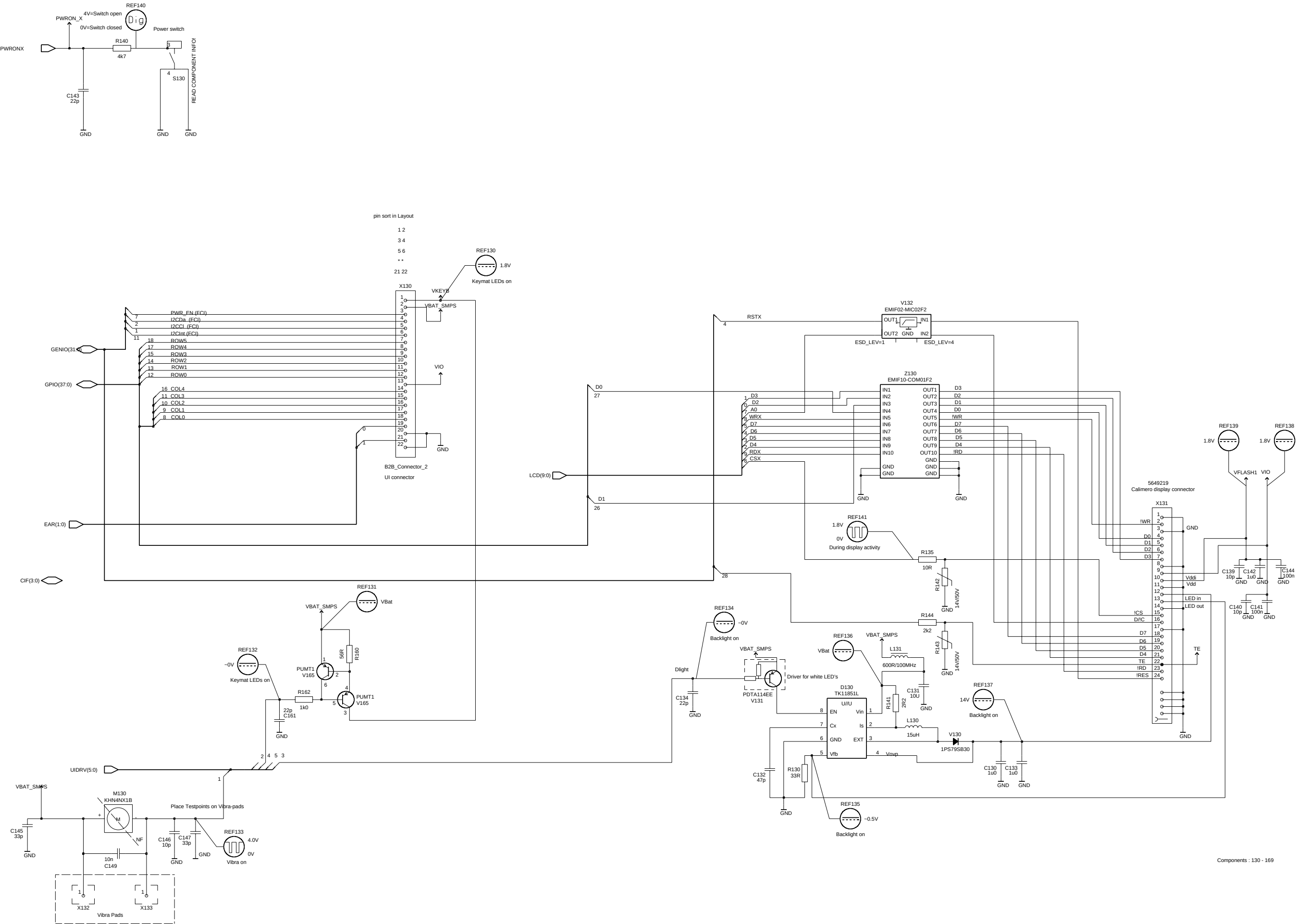
Layout diagram 1CK_10a, top



Layout diagram 1CK_10a, bottom



User Interface diagram 1CJ, v.1.0 ed.19



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